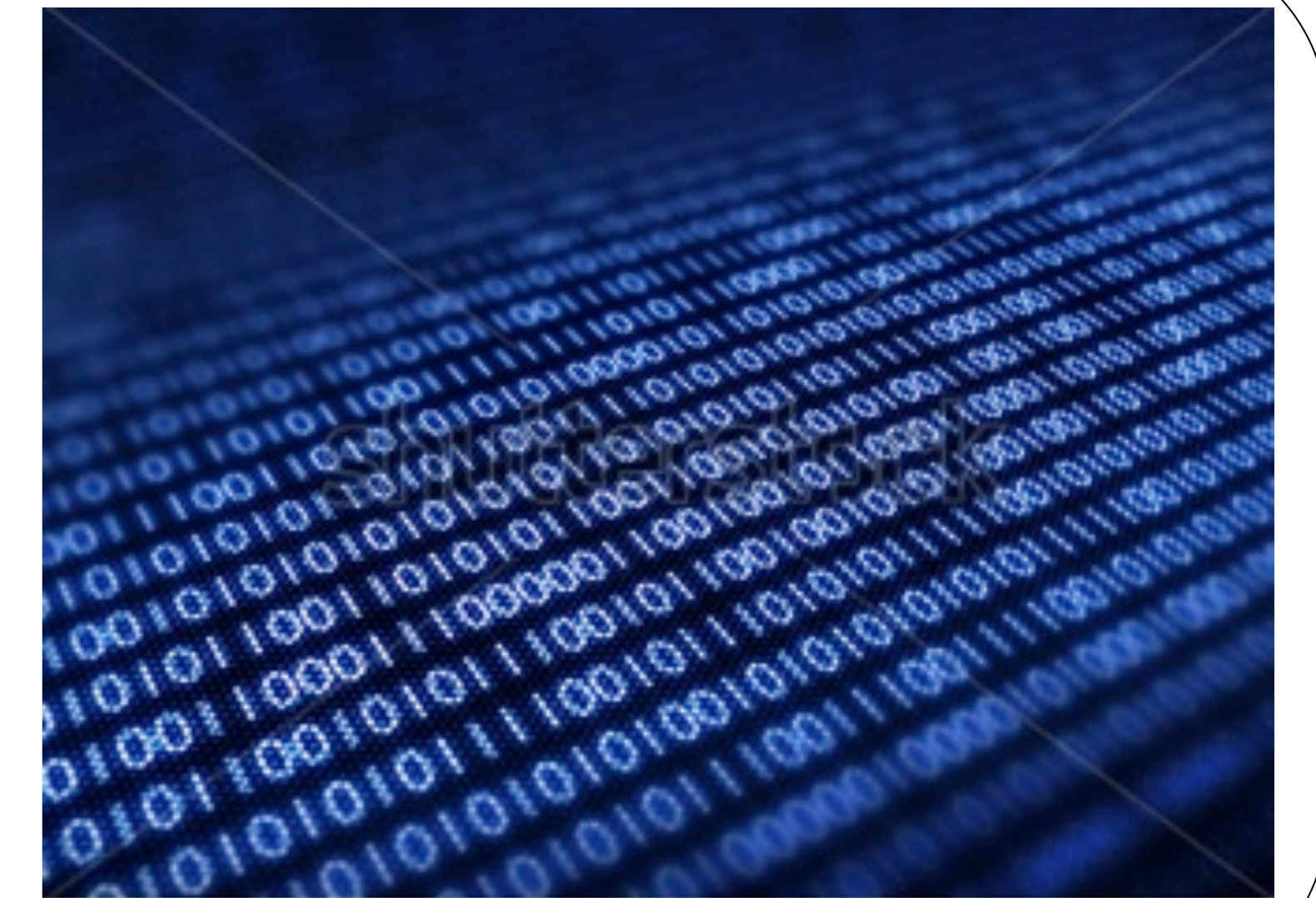


Analyzing ARM Native Code for Tracking Information Flow

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Introduction

- Third-party “apps” may leak users’ privacy-sensitive data or manifest malicious behavior.
- Why do we target ARM native code?
 - More and more apps use ARM native code.
 - Android : 49% of the apps are packaged with third-party native library.
 - Tizen : Native apps are written as ARM native code.
- Lots of studies about information flow tracking, but not in ARM-instruction level.



ARM Architecture

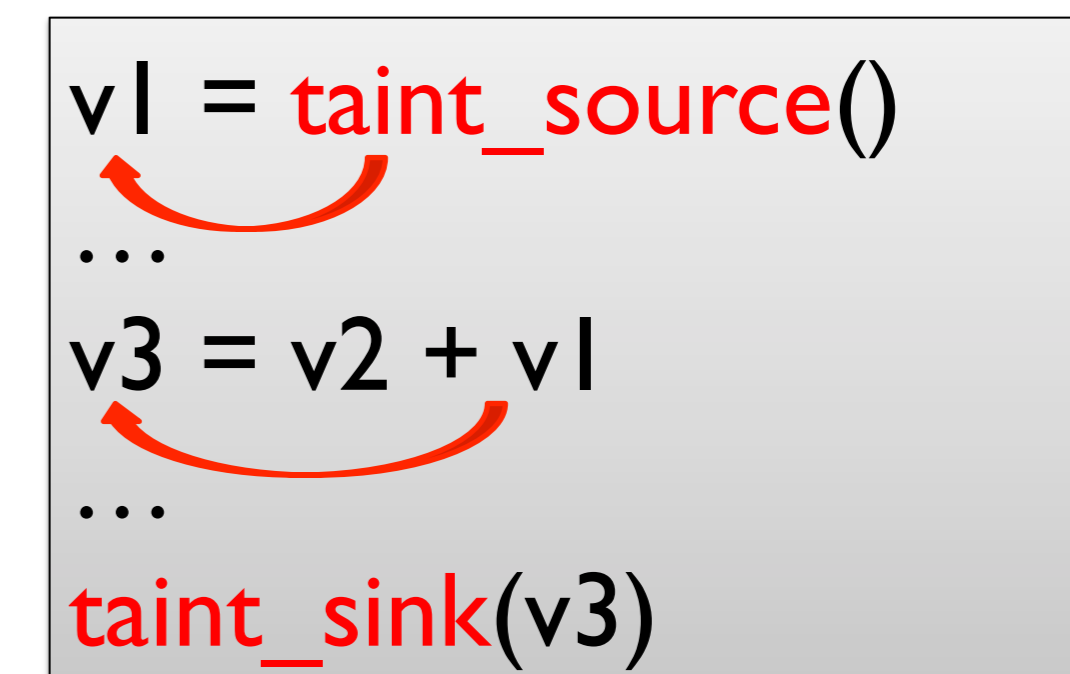
- Advanced RISC architecture
 - 32bit-fixed instruction length
 - PC as a general register
 - Single execution cycle
 - Conditional execution
- Extension
 - Thumb / Thumb-2 mode (16bit)

Some of these features are challenging to handle.

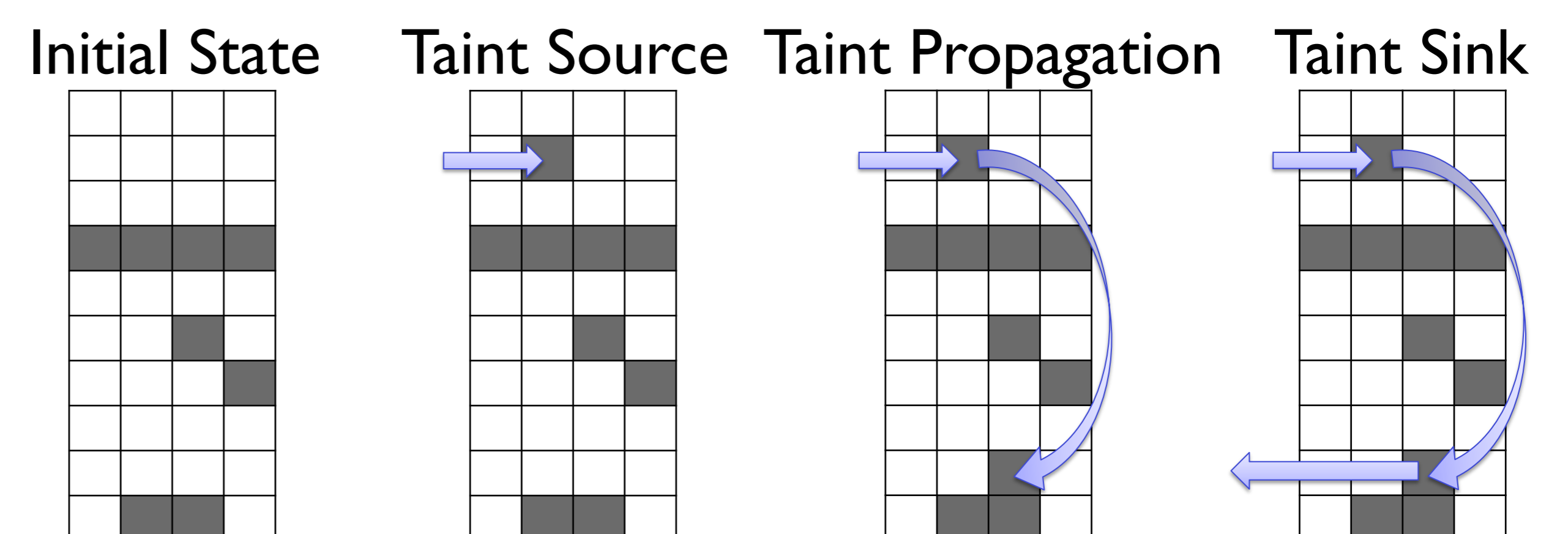
Dynamic Taint Tracking

- *Taint Tracking* is a technique used to track information dependencies from an origin.

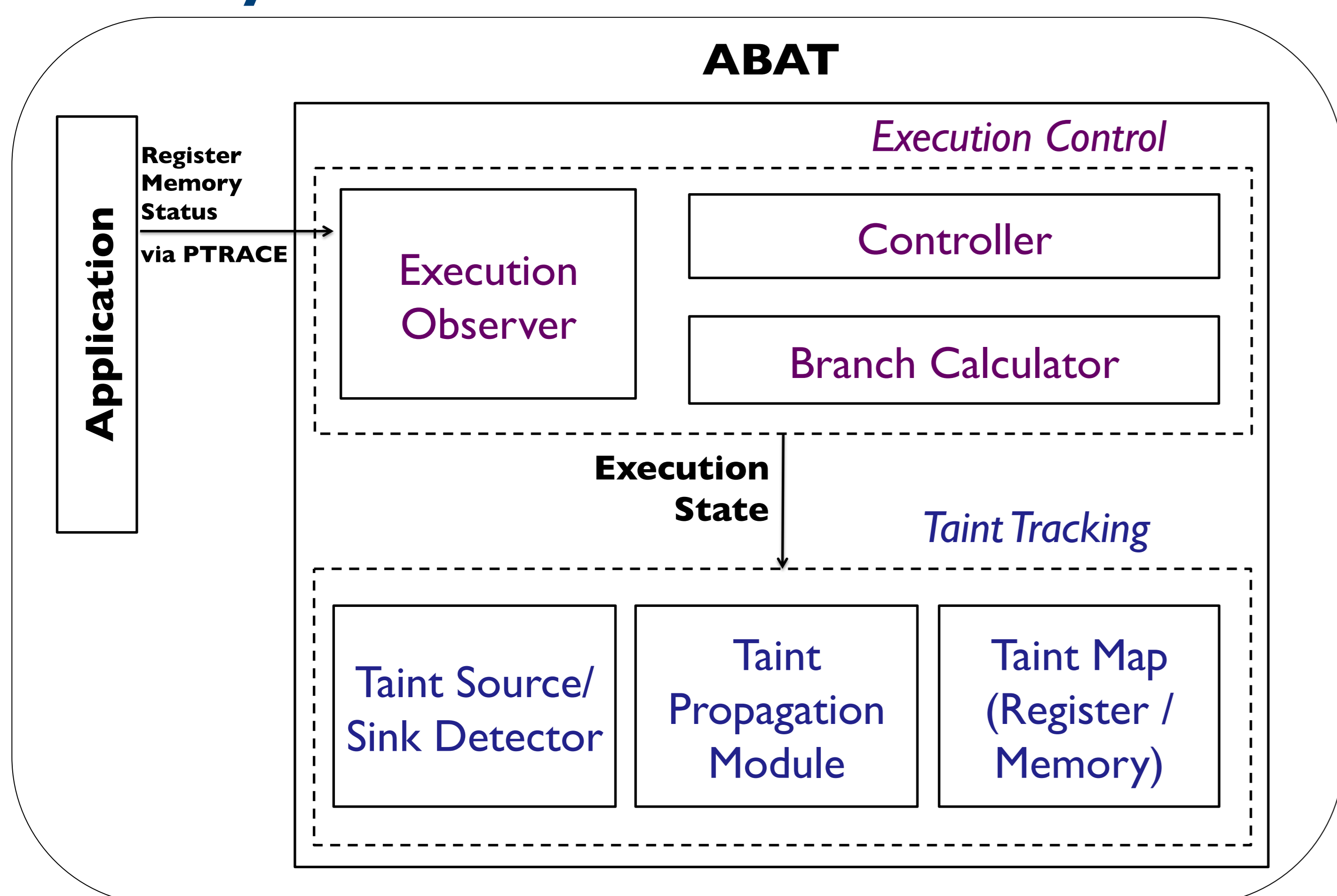
- Three Factors
 - Taint Source
 - Taint Propagation
 - Taint Sink



- Update Taint map during execution



System Architecture



- Taint Map API

Type	API	Description
Register	Tag source (threadId, regIndex, Set<Tag>);	Set an union of taint Tags to regIndex of threadId's register taint map
	Tag propagate (threadId, sourceIndex, destIndex);	Copy taint tag from sourceIndex to destIndex of threadId's register taint map
	Tag sink (threadId, regIndex);	Return Tag located in regIndex of threadId's register taint map
Memory	Tag source (memAddr, Set<Tag>);	Set an union of taint Tags to memAddr in memory taint map
	Tag propagate (sourceAddr, destAddr);	Copy taint tag from sourceAddr to destAddr of memory taint map
	Tag sink (memAddr);	Return Tag located in memAddr of memory taint map

Concurrency Handling

- Multi-thread application support
 - Different Taint Map(register) for each thread
 - Coherent Taint Map(memory) for whole threads

- Taint Propagation Logic

- We handle over 800 instructions

- Taint Map Function $\tau ()$

: $\tau (A)$ retrieves the taint tag for 'A' from Taint Map.

Operation Type	Assembly Representation	Action	Taint Propagation	Description
ADD <immediate>	ADD Rd, Rn, <immediate>	$Rd := Rn + \text{<immediate>}$	$\tau (Rd) \leftarrow \tau (Rn)$	Set Rd taint to Rn taint
ADD <register>	ADD Rd, Rn, Rm	$Rd := Rn + Rm$	$\tau (Rd) \leftarrow \tau (Rn) \cup \tau (Rm)$	Set Rd taint to Rn taint OR Rm taint
MOV <immediate>	MOV Rd, <immediate>	$Rd := \text{<immediate>}$	$\tau (Rd) \leftarrow \emptyset$	Clear Rd taint
MOV <register>	MOV Rd, Rn	$Rd := Rn$	$\tau (Rd) \leftarrow \tau (Rn)$	Set Rd taint to Rn taint